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(19) **United States**(12) **Patent Application Publication****Kim et al.**(10) **Pub. No.: US 2006/0103303 A1**(43) **Pub. Date: May 18, 2006**(54) **ORGANIC ELECTROLUMINESCENCE  
DISPLAY PANEL AND METHOD FOR  
SEALING THE SAME**(75) Inventors: **Chang Nam Kim**, Seoul (KR); **Jong  
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**CHANTILLY, VA 20153 (US)**(73) Assignee: **LG ELECTRONICS INC.**(21) Appl. No.: **11/320,845**(22) Filed: **Dec. 30, 2005****Related U.S. Application Data**(60) Continuation of application No. 10/627,816, filed on  
Jul. 28, 2003, now Pat. No. 7,011,562, which is a  
division of application No. 09/783,964, filed on Feb.  
16, 2001, now Pat. No. 6,624,572.(30) **Foreign Application Priority Data**

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**H05B 33/00** (2006.01)(52) **U.S. Cl.** ..... 313/512(57) **ABSTRACT**

An organic EL display panel has a multi-layered structure in which a first electrode and a second electrode are formed on a transparent panel and an organic EL layer is formed between the first and second electrodes. A method for sealing the organic EL display panel includes the steps of forming a buffer layer of an organic matter such as silicon oxide and silicon nitride on the transparent panel, and locating a shield cover on the buffer layer. Thus, adhesive strength between the panel and an adhesive is enhanced to prevent external humidity and oxygen from being permeated into the panel, thereby increasing life span of the display.

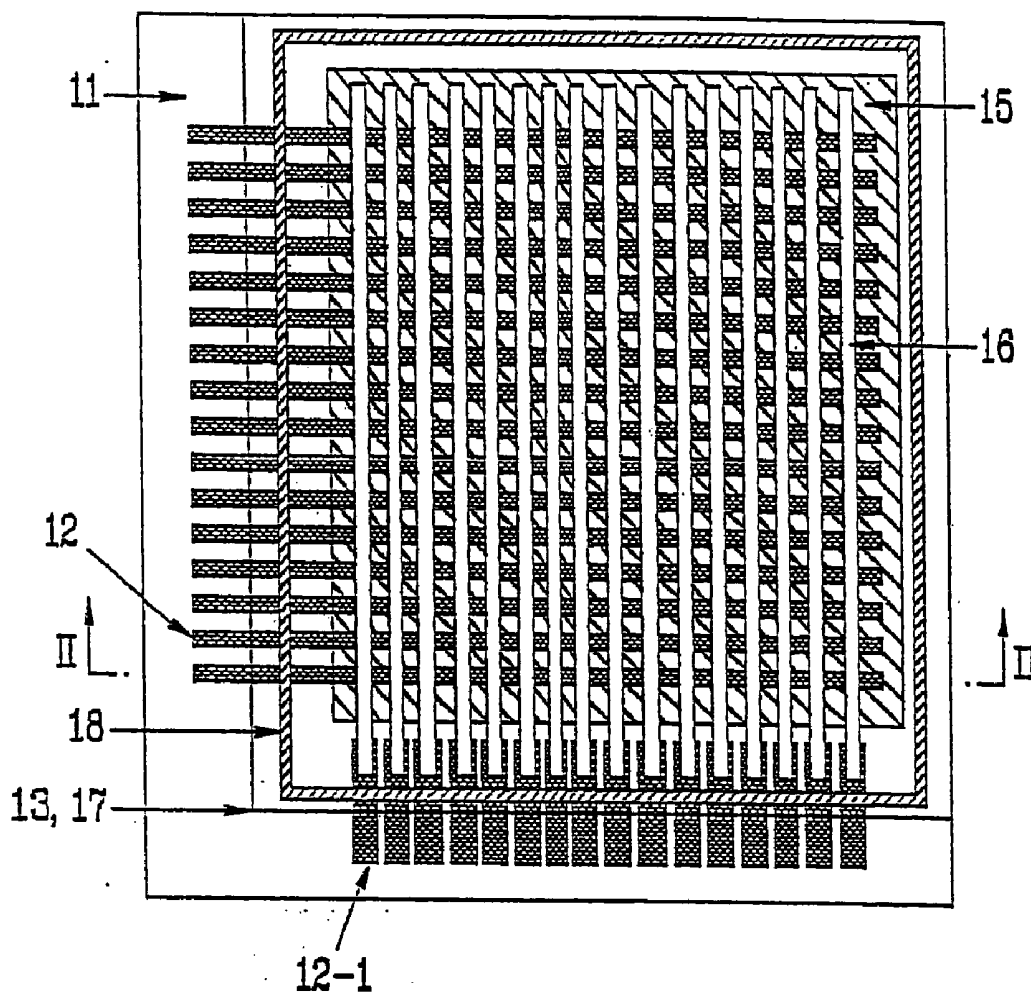


FIG. 1A  
Prior Art

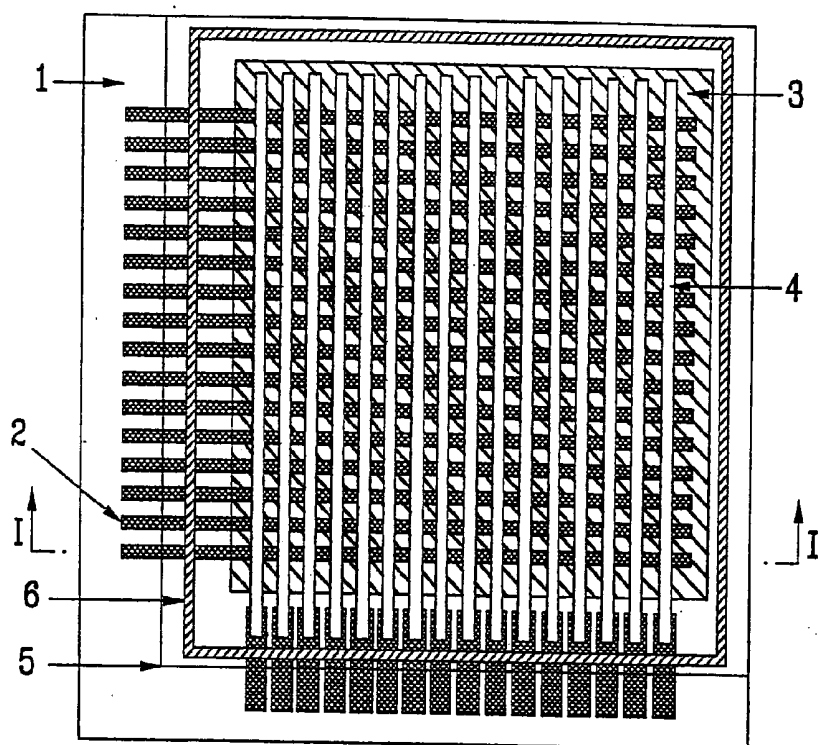


FIG. 1B  
Prior Art

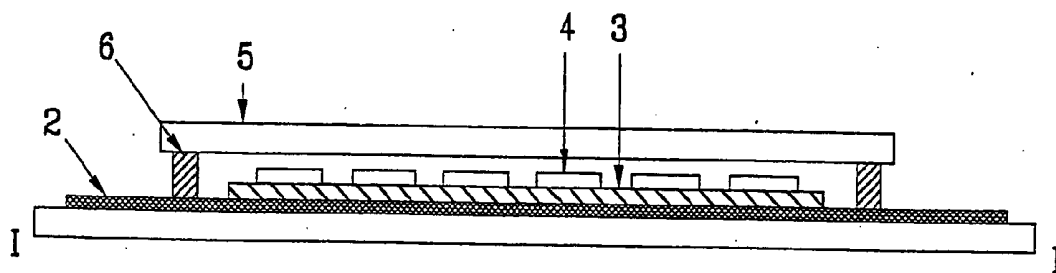


FIG.2A

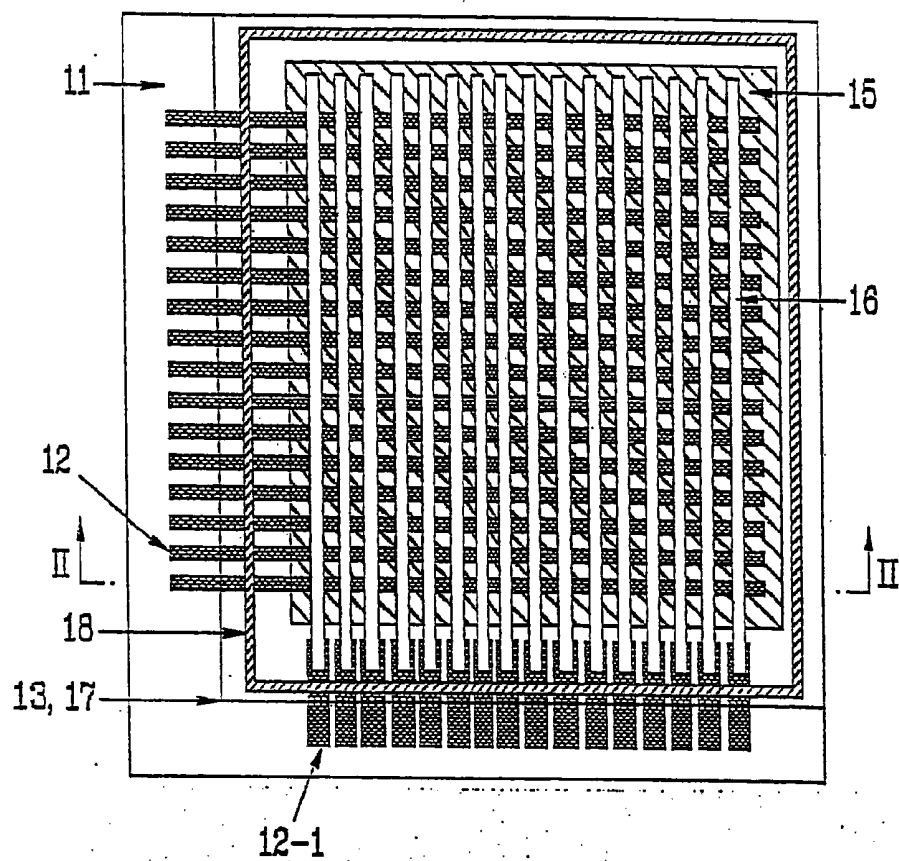


FIG.2B

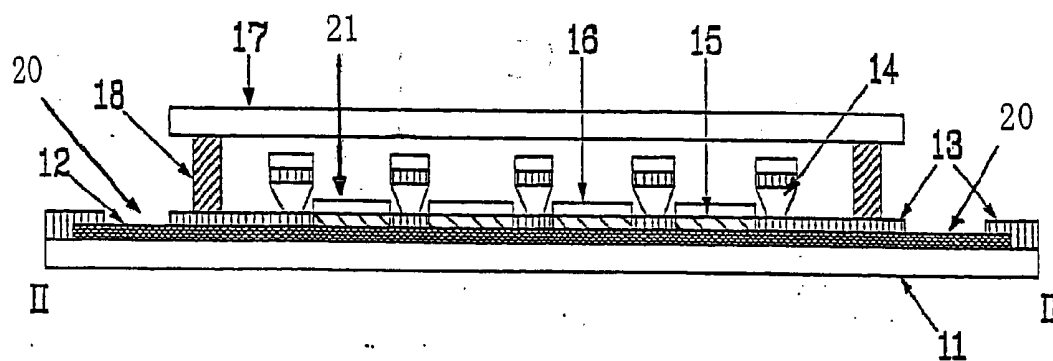


FIG. 3

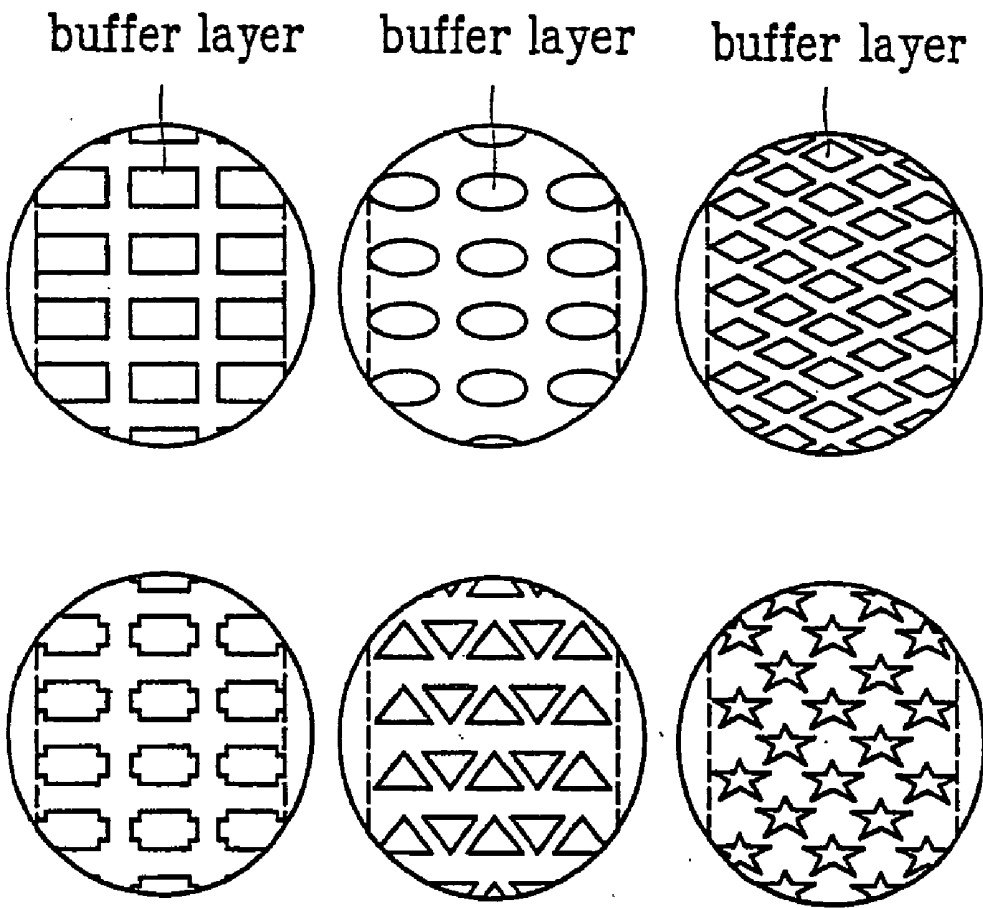


FIG. 4A

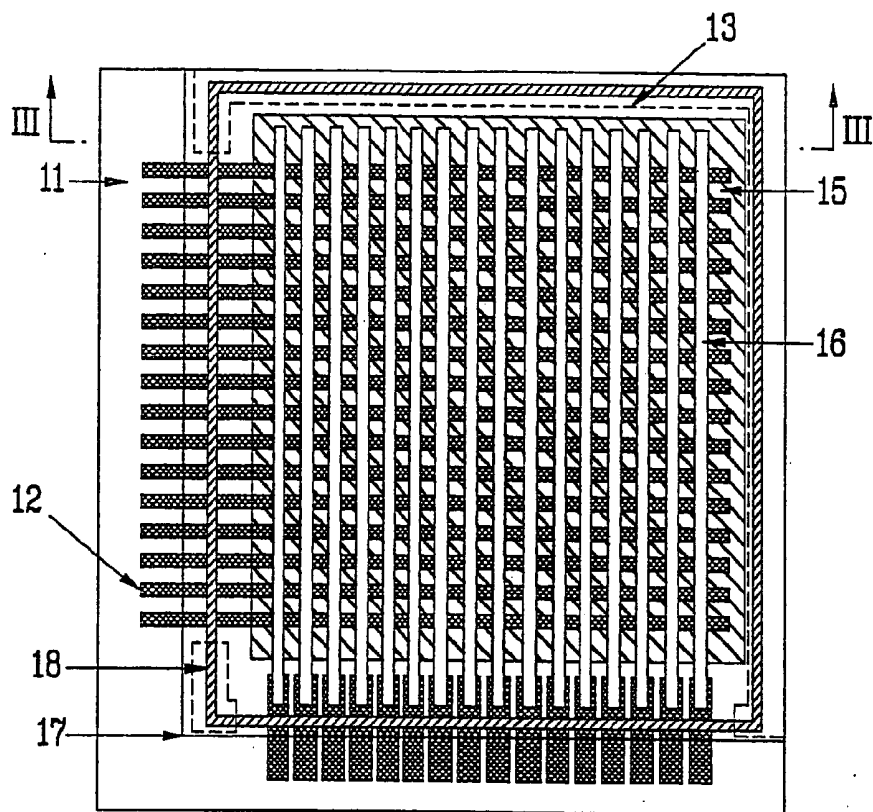


FIG. 4B

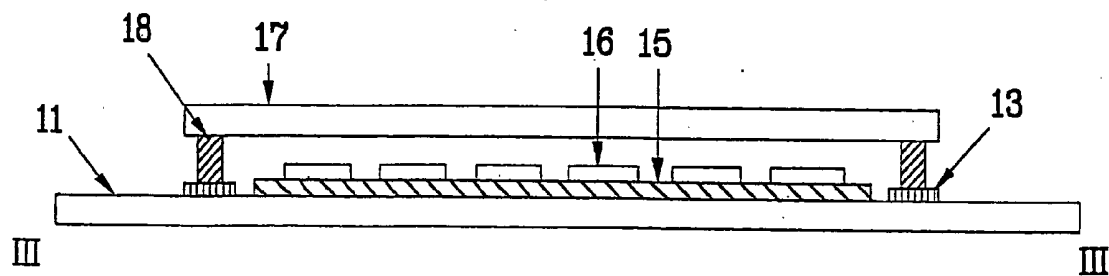


FIG. 5A

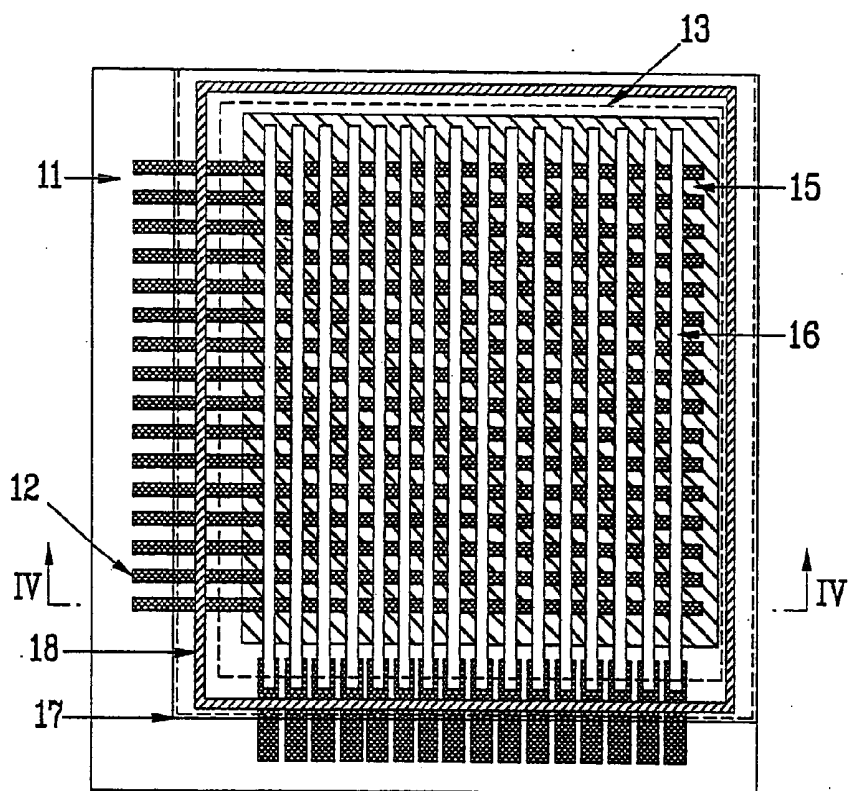
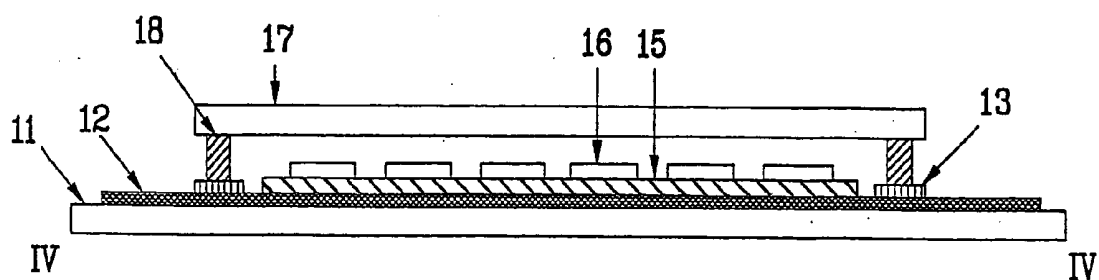


FIG. 5B



**ORGANIC ELECTROLUMINESCENCE DISPLAY PANEL AND METHOD FOR SEALING THE SAME**

[0001] This application is a continuation of U.S. application Ser. No. 10/627,816 filed Jul. 28, 2003, which is itself a continuation of U.S. application Ser. No. 09/783,964 which was filed Feb. 16, 2001 and which now has issued as U.S. Pat. No. 6,624,472 on Sep. 23, 2003. The disclosures of the previous applications are incorporated by reference herein.

**BACKGROUND OF THE INVENTION****[0002] 1. Field of the Invention**

[0003] The present invention relates to an organic electroluminescence (EL) display panel, and more particularly to an organic EL display panel and a method for sealing the same.

**[0004] 2. Description of the Related Art**

[0005] Recently, with a large sized tendency of a display device, request for a flat display occupied in a small space is increasing. As an example of the flat display, an EL display is receiving much attention.

[0006] The EL display is divided into an inorganic EL display and an organic EL display in accordance with its materials. Of them, the organic EL display emits light while electrons injected into an organic EL layer are combined with a hole and erased. The organic EL layer is formed between a cathode and an anode.

[0007] The organic EL display has an advantage in that it is driven at a lower voltage (for example, about 10V or less) than a voltage of a plasma display panel (PDP) or an inorganic EL display. Also, since the organic EL display has further advantages such as a wide viewing angle, high speed reactivity, and high contrast, it is used as a pixel of a graphic display, a television video display, and a surface light source. Moreover, since the organic EL display is thin and weighs light, and its color sensitivity is good, it is suitable for a next generation flat display.

[0008] However, the most serious problem in commercially using the organic EL display is that its life span is short.

[0009] The life span of the display is determined by various factors such as impurities within organic matters, interface between the organic matters and electrodes, a low crystallization temperature (T<sub>g</sub>) of the organic matters, and oxidation of the display due to oxygen and humidity. Problems related to such factors can be solved by refinement of the organic matters, development of a material having a high crystallization temperature, and induction of an organic metal to the interface between the electrodes and the organic matters. However, it is difficult to solve the problem related to oxidation of the display due to oxygen and humidity.

[0010] Oxygen and humidity that oxidize the display may exist in the display or may externally be permeated during the fabricating process steps. Oxygen and humidity are permeated into a pinhole of the display and oxidize an organic film and metal, thereby failing to perform a light emitting function of the display.

[0011] In the related art, to solve this problem, the display is covered with a shield glass to externally cut off humidity and oxygen.

[0012] In other words, as shown in **FIGS. 1a** and **1b**, an organic EL display provided with a glass panel **1**, a first electrode **2**, an organic EL layer **3**, and a second electrode **4** is covered with a shield glass **5** to cut off humidity and oxygen.

[0013] The shield glass **5** is attached to a corner portion of the panel by an adhesive **6**.

[0014] However, since adhesive strength of the adhesive **6** is not good for a glass panel, the adhesive **6** is detached from the panel after a constant time elapses.

[0015] If the adhesive **6** is detached from the panel, a gap occurs between the panel and the adhesive so that humidity or oxygen is permeated into the display. For this reason, a problem arises in that life span of the display is shortened.

**SUMMARY OF THE INVENTION**

[0016] To solve the above problems, an object of the present invention is to provide an organic EL display panel and a method for sealing the same, in which life span of the display can be improved by adhesive strength between a panel and an adhesive.

[0017] To achieve the above object, in an organic EL display panel having a multi-layered structure in which a first electrode and a second electrode are formed on a transparent panel and at least one organic EL layer is formed between them, the organic EL display panel according to the present invention includes a buffer layer formed on the transparent panel and a shield cover located on the buffer layer.

[0018] In another aspect, in a method for sealing an organic EL display panel having a multi-layered structure in which a first electrode and a second electrode are formed on a transparent panel and an organic EL layer is formed between the first and second electrodes, the method for sealing an organic EL display panel according to the present invention includes the steps of forming a buffer layer on the transparent panel, and locating a shield cover on the buffer layer.

[0019] The buffer layer is formed on an entire surface of the panel other than a tap bonding region and a pixel region of the first and second electrodes, or only on the panel of a region where the shield cover is located on the transparent panel, or both on the panel of a region where the shield cover is located and on the electrode of a region where the shield cover is located.

[0020] Also, the buffer layer of the region where the shield cover is located has an uneven shape or a dot shape. The dot shaped buffer layer is formed of any one of a round shape, a triangle shape, a quadrangle shape, a polygonal shape, and so on.

[0021] The buffer layer of the region where the shield cover is located and the buffer layer of a region other than the region where the shield cover is located have the same material as each other or a different material from each other. In other words, the buffer layer of the region where the shield cover is located includes a material of either silicon oxide or silicon nitride, while the buffer layer of the region other than the region where the shield cover is located includes a material of silicon oxide, silicon nitride, polyimide, or polyacryl.

[0022] In the present invention, an inorganic material such as silicon oxide and silicon nitride is formed on the panel of the region where the shield cover is located, so that adhesive strength between the panel and the adhesive is enhanced to prevent external humidity and oxygen from being permeated into the panel, thereby increasing life span of the display.

[0023] It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

#### BRIEF DESCRIPTION OF THE DRAWINGS

[0024] Other objects, characteristic features and advantages of the present invention will now become apparent with a detailed description of an embodiment made with reference to the accompanying drawings, in which:

[0025] **FIGS. 1a and 1b** are a plane view and a sectional view showing a related art method for sealing an organic EL display panel;

[0026] **FIGS. 2a and 2b** are a plane view and a sectional view showing a method for sealing an organic EL display panel according to the first embodiment of the present invention;

[0027] **FIG. 3** shows various uneven shapes of a buffer layer of **FIG. 2a**;

[0028] **FIGS. 4a and 4b** are a plane view and a sectional view showing a method for sealing an organic EL display panel according to the second embodiment of the present invention; and

[0029] **FIGS. 5a and 5b** are a plane view and a sectional view showing a method for sealing an organic EL display panel according to the third embodiment of the present invention.

#### DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

[0030] The following is a detailed description of a preferred embodiment of an organic EL display panel and a method for sealing the same according to the present invention made with reference to the accompanying drawings.

[0031] **FIGS. 2a and 2b** are a plane view and a sectional view showing a method for sealing an organic EL display panel according to the first embodiment of the present invention.

[0032] As shown in **FIGS. 2a and 2b**, to form an organic EL display, a transparent conductive film such as Indium Tin Oxide (ITO) is formed on a transparent insulating panel **11** such as glass. The transparent conductive film is patterned by photolithography process to form first electrodes **12** having a plurality of stripes and contact pads **12-1** for contact with second electrodes which will be formed later.

[0033] A buffer layer **13** is formed on an entire surface of the panel other than a tap bonding region **20** and a pixel **21** of the first electrode **12** and the contact pads **12-1**.

[0034] The buffer layer **13** acts different depending on its formation position. In other words, a function of the buffer layer formed in a corner portion of the panel **11** is different from that of the buffer layer formed around the pixel region.

[0035] The buffer layer formed in a corner portion of the panel **11** acts to enhance adhesive strength of an adhesive when a shield cover is attached to the panel. The buffer layer formed around the pixel region acts to perform electrical insulation between the first electrode and the second electrode and between the second electrodes.

[0036] A region of the buffer layer formed in a corner portion of the panel **11** (i.e., a region where the shield cover is located) may have an uneven shape or a dot shape, as shown in **FIG. 3**, to enhance adhesive strength of the adhesive.

[0037] That is, the dot shaped buffer layer may be formed of any one of various shapes such as a round shape, a triangle shape, a quadrangle shape, and a polygonal shape.

[0038] The buffer layer formed around the pixel region may have a stripe shape in a direction orthogonal to the first electrode **12**. As described above, since the buffer layer **13** acts differently depending on its position, one material or different materials may be used as the buffer layer **13**.

[0039] In other words, the material of the buffer layer formed in a corner portion of the panel (buffer layer of a region where the shield cover is located) is an inorganic matter such as silicon oxide and silicon nitride. The material of the buffer layer formed around the pixel region (buffer layer of a region other than the region where the shield cover is located) may be either an inorganic matter such as silicon oxide and silicon nitride or an organic matter such as polyimide, and polyacryl.

[0040] At this time, the buffer layer has a thickness of about 0.1~5  $\mu\text{m}$ .

[0041] Next, for electrical insulation between second electrode stripes which will be formed later, electrical insulation barrier stripes **14** are formed on the buffer layer **13**.

[0042] Subsequently, organic EL layers **15** are formed on the electrical insulation barrier stripes **14**.

[0043] The organic EL layers **15** are formed to be wider than an entire light-emitting region (an entire region where the first and second electrodes cross orthogonally). However, the organic EL layers **15** should be formed inwardly rather than end portions of the contact pads **12-1**.

[0044] A conductive material such as Al, Mg:Ag, and Al:Li is formed to form second electrodes **16**. Thus, the fabrication of the organic EL display is completed.

[0045] Here, the second electrodes **16** are formed to be wider than the light-emitting region and to be connected with the contact pads **12-1**.

[0046] Subsequently, the organic EL display is covered with a shield cover **17** to cut off humidity and oxygen. The shield cover **17** is attached to the buffer layer **13**, which is formed in a corner portion of the panel, by an adhesive **18**. A UV hardening high molecular substance is used as the adhesive **18**. In addition, other adhesives such as a thermal hardening adhesive or an inorganic adhesive may be used.

[0047] After the buffer layer is formed as described above, if the shield cover is attached to the buffer layer by the adhesive, adhesive strength of the adhesive is enhanced, thereby failing to permeate external humidity or oxygen into the organic EL display.



[0048] **FIGS. 4a and 4b** are a plane view and a sectional view showing a method for sealing an organic EL display panel according to the second embodiment of the present invention.

[0049] In the second embodiment of the present invention, a buffer layer is only formed on a panel of a region where a shield cover is located.

[0050] Generally, since a sealing adhesive does not have good adhesive strength with the panel, the buffer layer is formed on only the panel of the region where the shield cover is located.

[0051] Also, the buffer layer may have an uneven shape or a dot shape to enhance adhesive strength of the adhesive in the same manner as the first embodiment.

[0052] In the second embodiment, since the buffer layer is only formed on the panel of the region where the shield cover is located, the process is simplified. However, in this case, either a display structure which does not require an electrical insulation barrier or a display structure which enables electrical insulation between second electrodes by an electrical insulation barrier only without a buffer layer is required.

[0053] **FIGS. 5a and 5b** are a plane view and a sectional view showing a method for sealing an organic EL display panel according to the third embodiment of the present invention.

[0054] In the third embodiment of the present invention, a buffer layer is formed on both a panel of a region where a shield cover is located and an electrode of a region which the shield cover is located.

[0055] Also, the buffer layer may have an uneven shape or a dot shape to enhance adhesive strength of the adhesive in the same manner as the first embodiment.

[0056] In the third embodiment, the process is also simplified. However, either a display structure which does not require an electrical insulation barrier or a display structure which enables electrical insulation between second electrodes by an electrical insulation barrier only without a buffer layer is required.

[0057] As aforementioned, the organic EL display panel and the method for sealing the same according to the present invention have the following advantages.

[0058] The inorganic matter such as silicon oxide and silicon nitride is formed on the panel of the region where the shield cover is located, so that adhesive strength between the panel and the adhesive is enhanced to prevent external humidity and oxygen from being permeated into the panel, thereby increasing life span of the display.

[0059] The above description will enable one skilled in the art to modify and revise the invention within the spirit not degrading the technical concept of the present invention. However, the technical scope of the present invention is not limited to the above description of the embodiment but shall be determined by the claims.

What is claimed is:

1. An EL device comprising:
  - an insulating panel;
  - electrodes formed on the insulating panel;
  - a shield cover located in a spaced apart relation from the insulating panel and the electrodes;
  - a buffer layer formed on at least a portion of the electrodes and the insulating panel in a surrounding region of the shield cover; and
  - an adhesive layer located between the buffer layer and the shield cover to attachment between the insulating panel and the shield cover.
2. The EL device as claimed in claim 1, wherein the buffer layer is non-conductive.
3. The EL device as claimed in claim 1, wherein the material of the buffer is either silicon oxide or silicon nitride.
4. The EL device as claimed in claim 1, wherein the buffer layer increases the adhesive strength between the insulating panel and the shield cover, and prevents the permeation of external humidity or oxygen.
5. The EL device as claimed in claim 1, wherein the buffer layer has an uneven or discontinuous surface.
6. The EL device as claimed in claim 1, wherein the buffer layer has a thickness of about 0.1-5  $\mu\text{m}$ .
7. The EL device as claimed in claim 1, wherein the electrodes are contact pads for connecting with an external power source.
8. The EL device as claimed in claim 1, wherein the electrodes are anode lines or cathode lines.
9. The EL device as claimed in claim 1, wherein the electrodes are gate lines, source lines or drain lines.
10. The EL device as claimed in claim 1, wherein the EL device is a television, computer, monitor or graphic display.
11. An EL device comprising:
  - an insulating panel;
  - electrodes formed on the insulating panel;
  - a shield cover located in a spaced apart relation from the insulating panel and the electrodes;
  - a first buffer layer formed on at least a portion of the electrodes and the insulating panel in a surrounding region of the shield cover;
  - a second buffer layer formed on at least a portion of the electrodes and the insulating panel in a middle region of the shield cover; and
  - an adhesive layer located between the first buffer layer and the shield cover for attaching the insulating panel and the shield cover.
12. The EL device as claimed in claim 11, wherein the first buffer layer and the second buffer layer comprise the same or different materials.
13. The EL device as claimed in claim 11, wherein the material of the first buffer is either silicon oxide or silicon nitride, and wherein the material of the second buffer is any one of silicon oxide, silicon nitride, polyimide, and polyacryl.
14. The EL device as claimed in claim 11, wherein the first buffer layer increases the adhesive strength between the insulating panel and the shield cover, and prevents the permeation of external humidity or oxygen.

15. The EL device as claimed in claim 11, wherein the second buffer layer performs electrical insulation between the electrodes.

16. An EL device comprising:

an insulating panel;

first electrodes formed on the insulating panel;

at least one of EL layer formed on the first electrodes;

second electrodes formed on the at least one EL layer;

a shield cover located in a spaced apart relation from the insulating panel and the electrodes;

a first buffer layer formed on at least a portion of the first electrodes and the insulating panel in a surrounding region of the shield cover;

a second buffer layer formed on at least a portion of the first electrodes and the insulating panel in a middle region of the shield cover; and

an adhesive layer located between the first buffer layer and the shield cover for attaching the insulating panel and the shield cover.

17. The EL device as claimed in claim 16, further comprising:

electrical insulation barrier stripes located on the second buffer layer between the second electrodes.

18. The EL device as claimed in claim 16, wherein the first buffer layer and the second buffer layer comprise the same or different materials.

19. The EL device as claimed in claim 16, wherein the material of the first buffer is either silicon oxide or silicon nitride, and wherein the material of the second buffer is any one of silicon oxide, silicon nitride, polyimide, and polyacryl.

20. The EL device as claimed in claim 16, wherein the first buffer layer increases the adhesive strength between the insulating panel and the shield cover, and prevents the permeation of external humidity or oxygen.

21. The EL device as claimed in claim 16, wherein the second buffer layer performs electrical insulation between the first electrode and the second electrode and between the second electrodes.

22. An EL device comprising:

an insulating panel;

first electrodes formed on the insulating panel;

at least one EL layer formed on the first electrodes;

second electrodes formed on the at least one EL layer;

a shield cover located in a spaced apart relation from the insulating panel and the electrodes;

a buffer layer formed on an entire surface of the insulating panel and the first electrodes other than a tap bonding region and a pixel region of the first electrodes; and

an adhesive layer located between the buffer layer and the shield cover for attaching the insulating panel and the shield cover.

23. The EL device as claimed in claim 22, further comprising:

electrical insulation barrier stripes located between the second electrodes.

24. The EL device as claimed in claim 22, wherein the buffer layer is non-conductive.

25. The EL device as claimed in claim 22, wherein the material of the buffer layer is any one of silicon oxide, silicon nitride, polyimide, and polyacryl.

26. The EL device as claimed in claim 22, wherein the buffer layer increases the adhesive strength between the insulating panel and the shield cover, and prevents the permeation of external humidity or oxygen.

27. The EL device as claimed in claim 22, wherein the buffer layer performs electrical insulation between the first electrode and the second electrode and between the second electrodes.

\* \* \* \* \*

专利名称(译)	有机电致发光显示板及其密封方法		
公开(公告)号	<a href="#">US20060103303A1</a>	公开(公告)日	2006-05-18
申请号	US11/320845	申请日	2005-12-30
申请(专利权)人(译)	LG电子株式会社.		
当前申请(专利权)人(译)	微软技术Licensing , LLC公司		
[标]发明人	KIM CHANG NAM YOON JONG GEON		
发明人	KIM, CHANG NAM YOON, JONG GEON		
IPC分类号	H05B33/00 G06Q40/02 H01L27/32 H01L51/52		
CPC分类号	G06Q40/02 H01L27/3281 H01L27/3283 H01L27/3295 H01L51/5237 H01L51/5246 H01L51/5253 H05B33/04 H01L51/524		
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其他公开文献	US7176624		
外部链接	<a href="#">Espacenet</a> <a href="#">USPTO</a>		

#### 摘要(译)

有机EL显示面板具有多层结构, 其中第一电极和第二电极形成在透明面板上, 有机EL层形成在第一和第二电极之间。一种密封有机EL显示板的方法, 包括在透明板上形成有机物质如氧化硅和氮化硅的缓冲层, 以及在缓冲层上设置屏蔽罩的步骤。因此, 增强了面板和粘合剂之间的粘合强度, 以防止外部湿气和氧气渗透到面板中, 从而增加了显示器的寿命。

